Appl. No. 10/828,910
Amdt. dated April 4, 2006
Reply to Office action of February 22, 2006

Amendments to the Specification:

Please replace paragraph [0014] with the following amended paragraph:

[0014] CPU 10 executes software stored in memory 14 or other storage devices. Under the direction of the software, CPU 10 may accept commands from an operator via keyboard 26 [[24]] or an alternative input device, and may display desired information to the operator via a display 25 or an alternative output device. Bridge 12 coordinates the flow of data between components such as between CPU 10 and slots 18A-B.

Please replace paragraph [0018] with the following amended paragraph:

[0018] Figure 2 depicts an exemplary system 30 comprising. System 30 includes devices 32A-B communicating with each other serially via a link 34. Accordingly, system 30 may implement the PCI-Express standard or any other standard capable of performing serial communications. Device 32A may be a PCI-Express compliant device inserted into slot 18A. Device 32B may comprise a bridge that is PCI-Express compliant, such as bridge 12.